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**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Akihiko EBINA et al.

Application No.: 10/052,255

Filed: January 23, 2002

Docket No.: 111778

For: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR  
MEMORY DEVICES

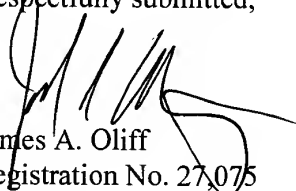
**SUBMISSION OF ORIGINAL DECLARATION**

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

Submitted herewith is the original signed Declaration of the Inventor. This  
Declaration corresponds to the facsimile copy of the Declaration filed on April 19, 2002.

Respectfully submitted,

  
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Date: April 26, 2002

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